

Digital I/O

Number of channels 8

Compatibility TTL

Power-on state Input (high impedance)

Digital logic levels

Level	Min	Max
Input low voltage	—	0.8 V
Input high voltage	2.0 V	—
Output low voltage	—	0.45 V, $I_{OL} = 16$ mA
Output high voltage	2.4 V, $I_{OH} = 16$ mA	—
Input leakage current	—	10 μ A

Bus Interface

Type Slave

Power Requirement

+5 V

NI 6703 1.1 A

NI 6704 1.8 A

+12 V 70 mA

–12 V 70 mA



Note These power usage figures do not include the power used by external devices that are connected to the fused supply present on the I/O connector. They assume that all voltage and current outputs are fully loaded.